

REMARKS

Claims 19-36 are all the claims pending in the application. Claims 1-18 have been canceled without prejudice or disclaimer and claims 19-36 have been newly added herewith.

Claims 1-18 stand rejected under 35 U.S.C. § 102(b) as being anticipated by Horst et al. (U.S. Patent No. 5,751,932). Inasmuch as the Examiner would now attempt to cite Horst against claims 29-36, Applicant submits that these claims are allowable over Horst.

Claim 19 is allowable at least because Horst fails to teach or suggest a controller which controls processors as set forth in the claim in combination with the other features recited therein. For example, with reference to Horst Fig. 2, even if the element 20b could be considered a processor, it does not operate as claimed. The Horst processor 20b does not work based upon a first memory on the same computer module during a normal process and by means of a second memory on the same computer module during a rejoining process as claimed. As shown in Fig. 2, the processor 20b appears to be always connected to the alleged second memory, cache 22. There is no indication of processors controlled to operate based on different memories as claimed. Accordingly, claim 19 is allowable over Horst.

Claims 20-36 depend from claim 19 and are therefore allowable at least by virtue of their dependency.

Conclusion

In view of the above, reconsideration and allowance of this application are now believed to be in order, and such actions are hereby solicited. If any points remain in issue which the

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Examiner feels may be best resolved through a personal or telephone interview, the Examiner is kindly requested to contact the undersigned at the telephone number listed below.

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

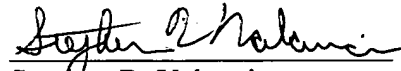
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